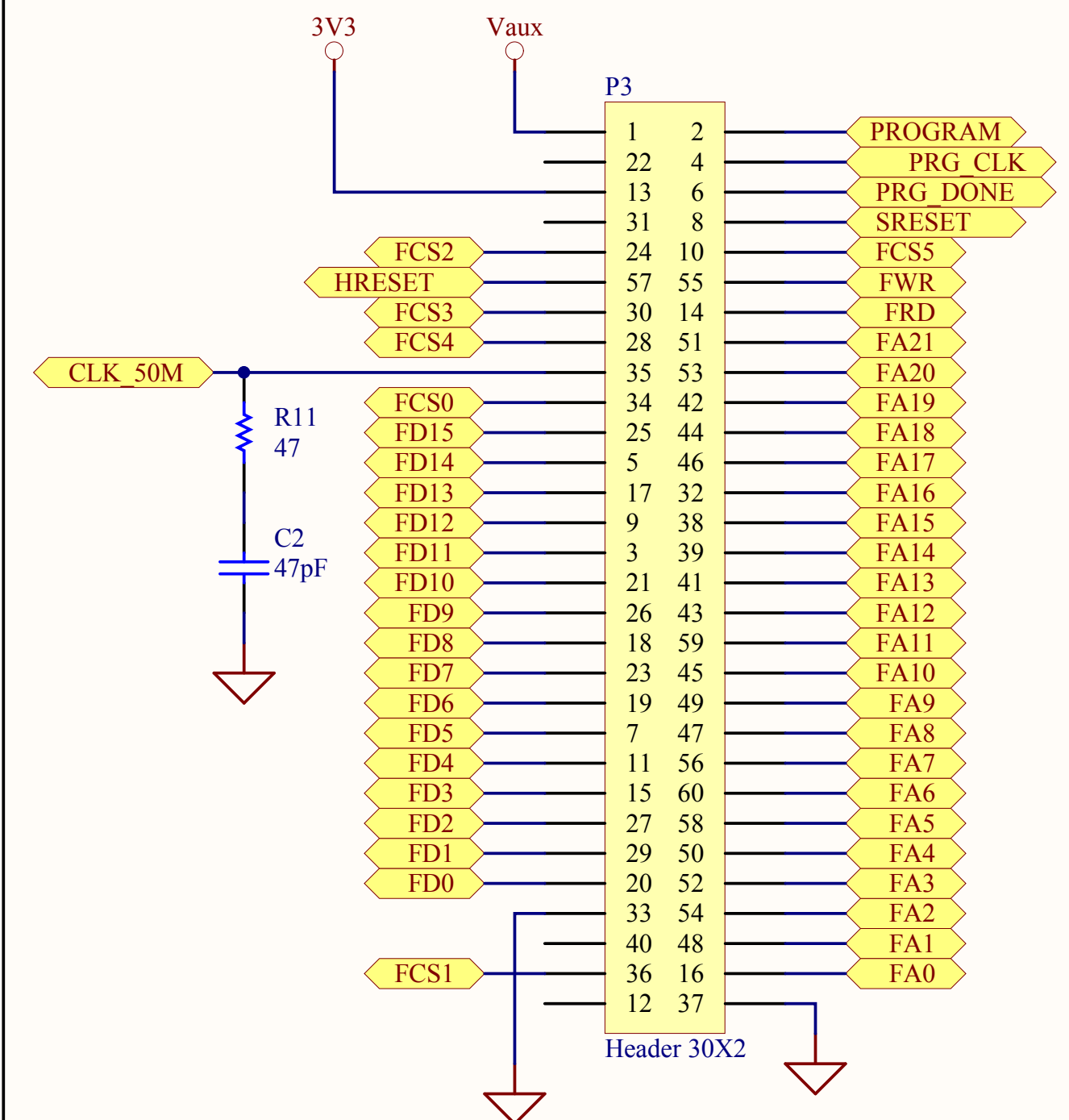
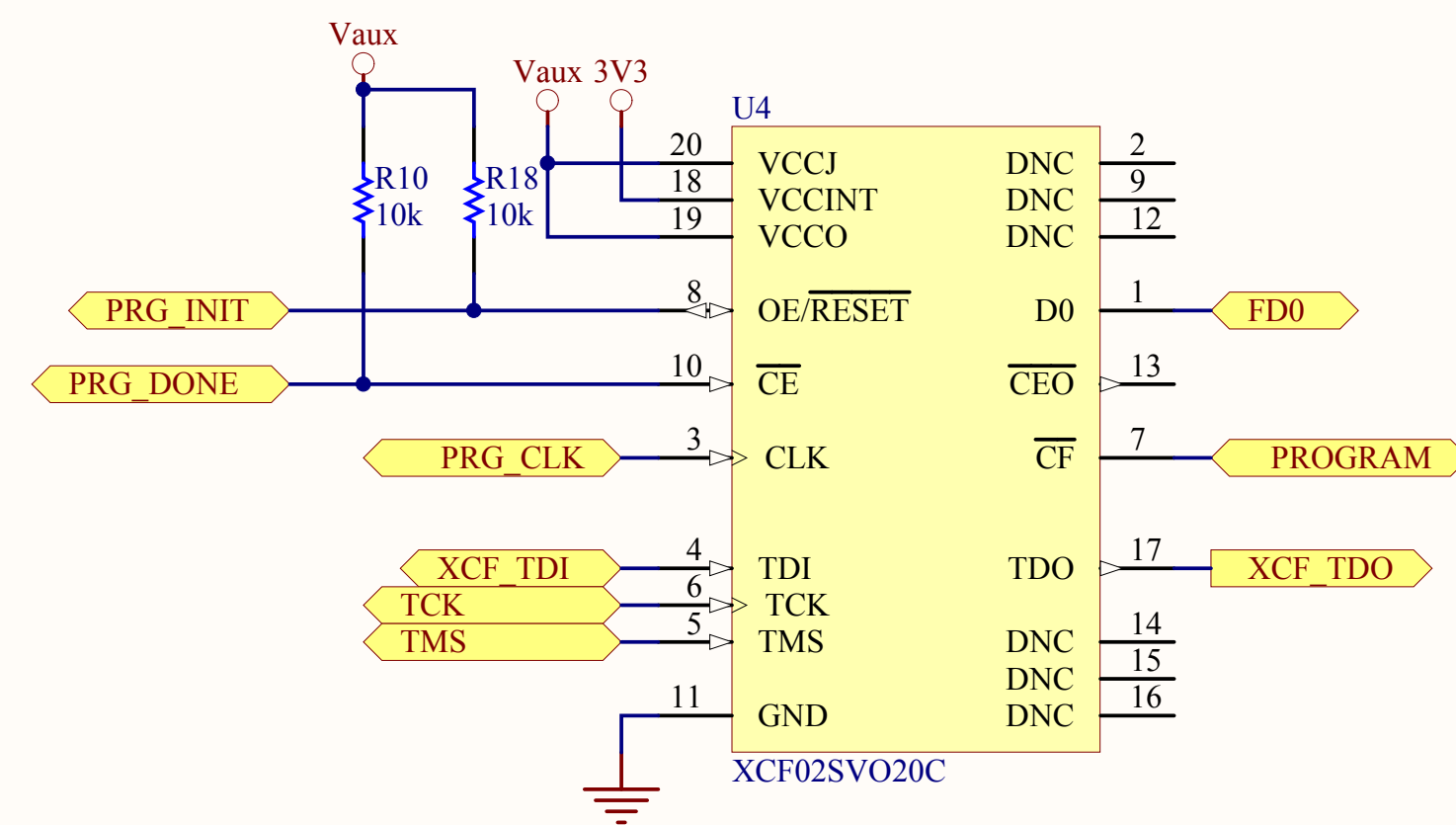
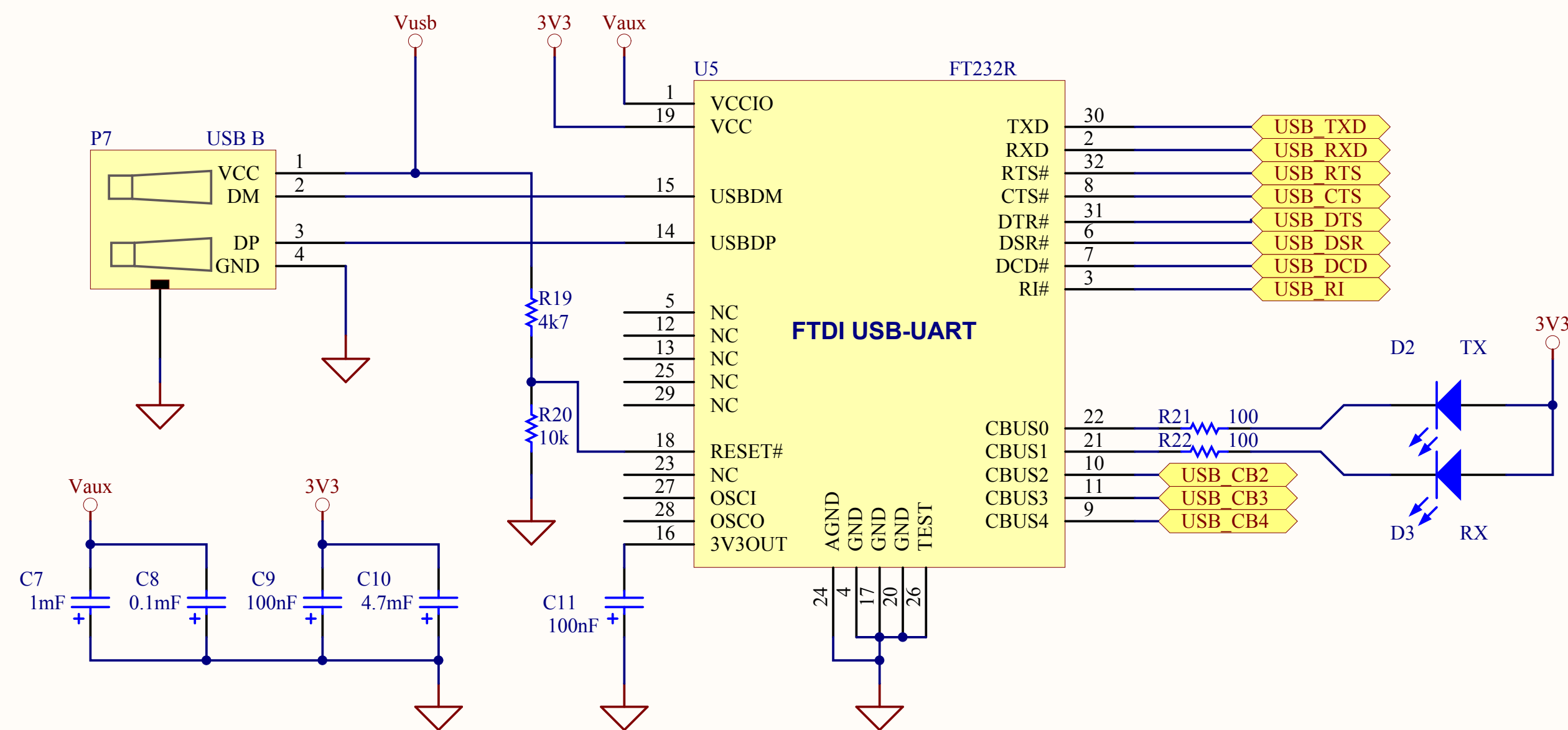
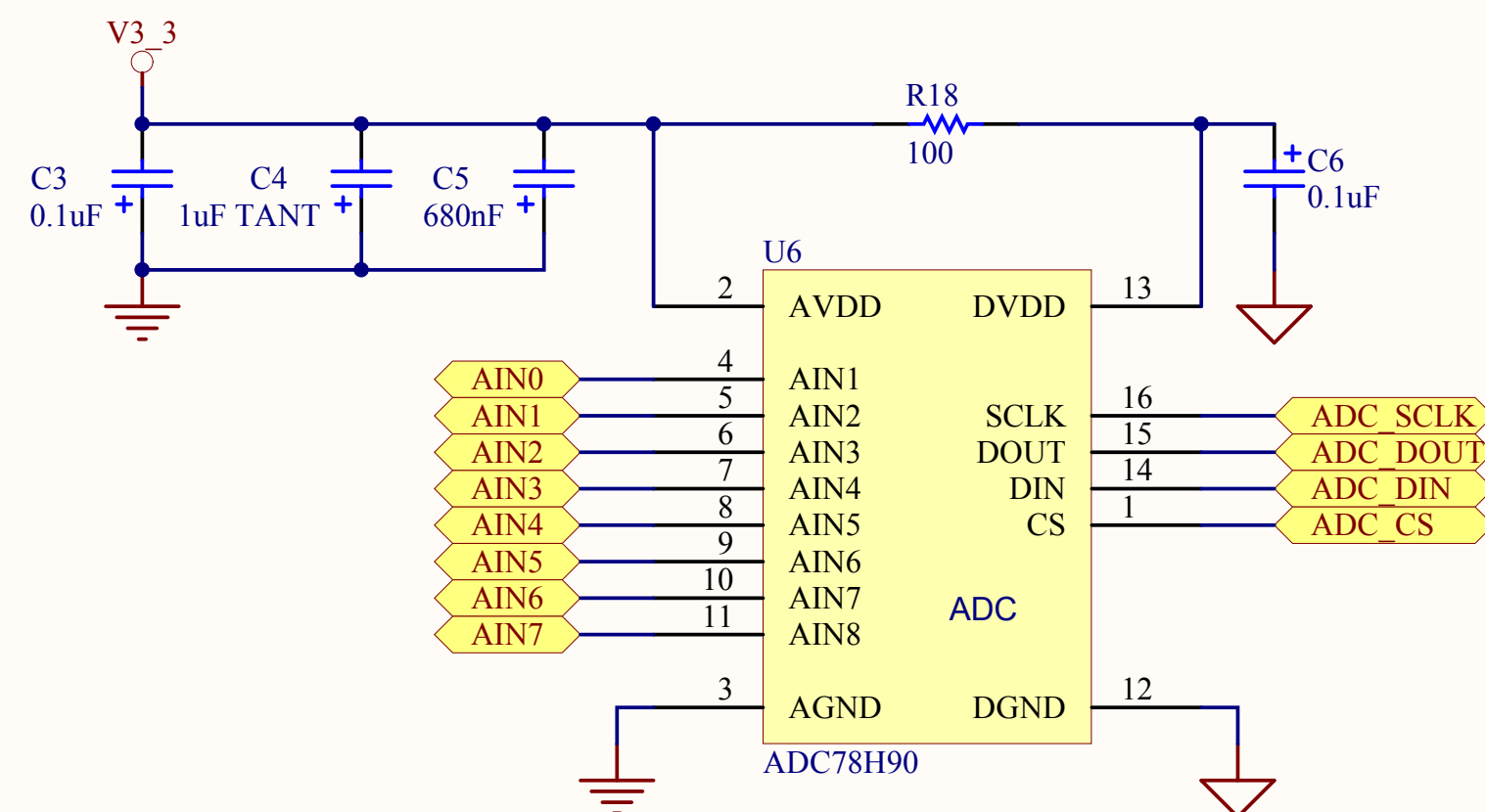
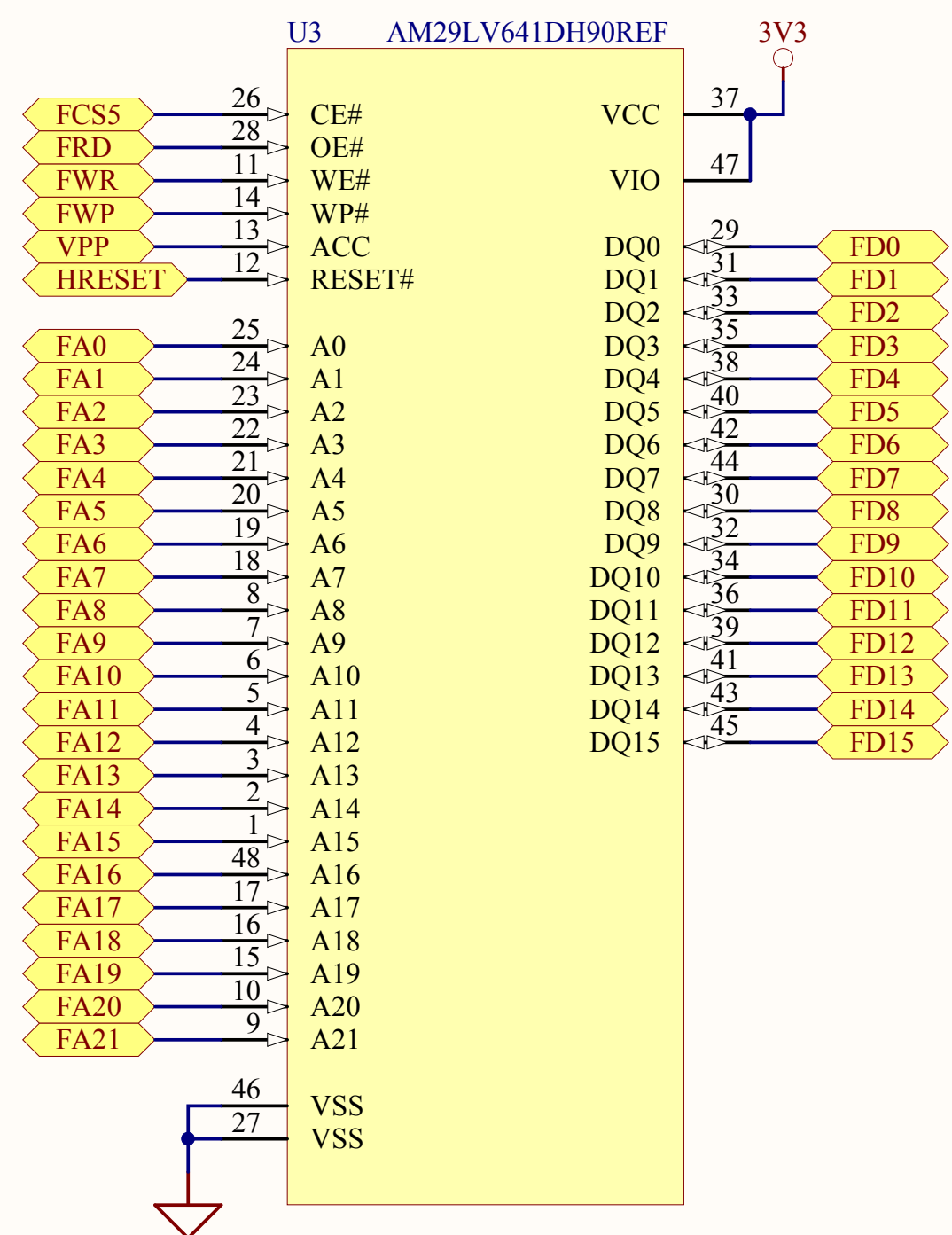


USB - UART

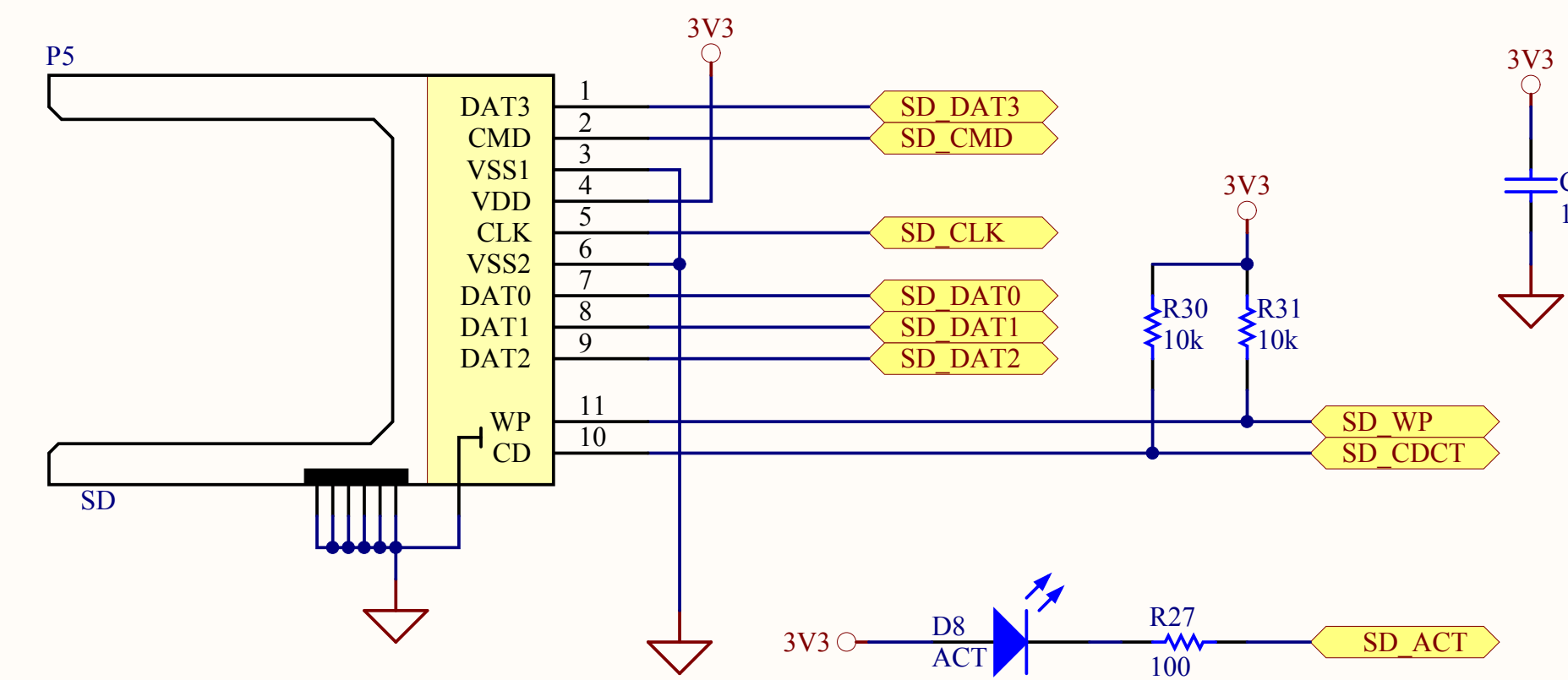


ETHERNET 10/100MB/s

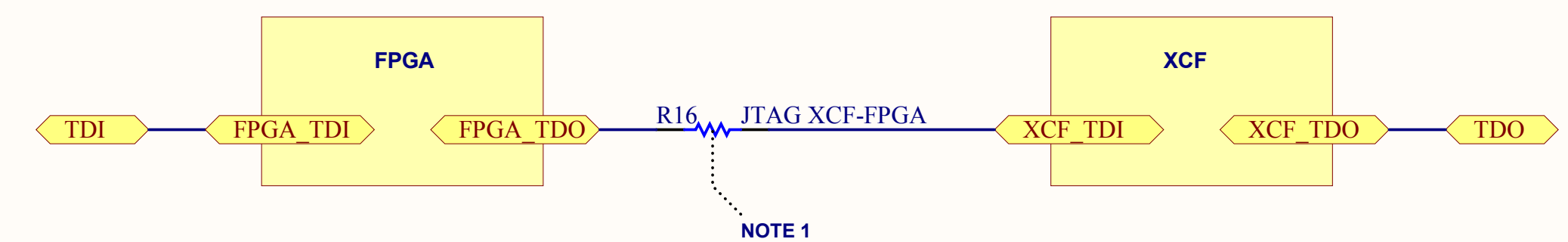
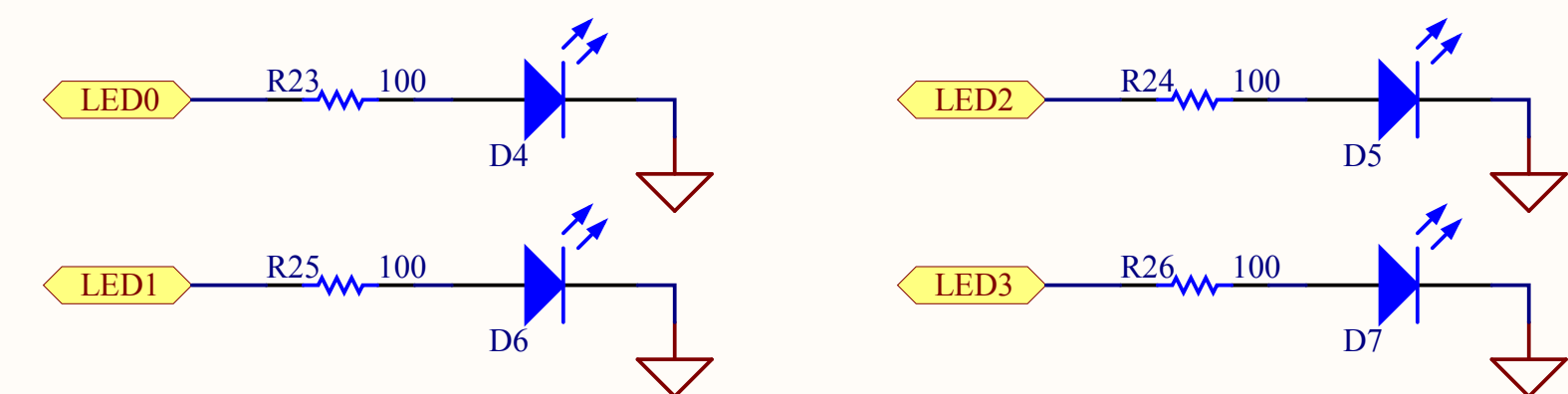
FLASH ROM



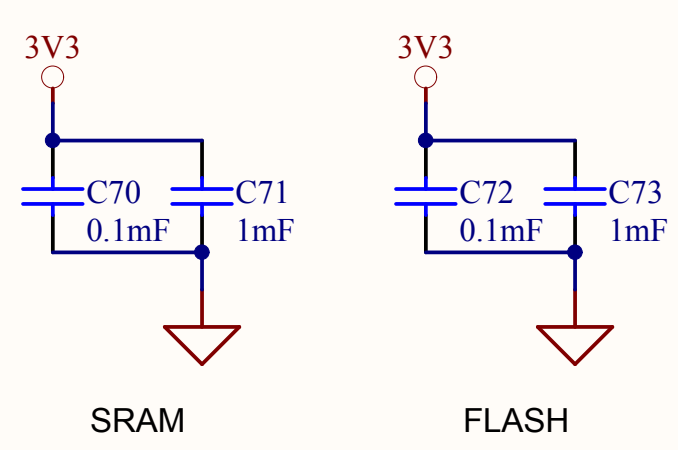
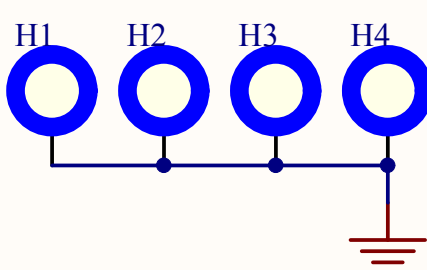
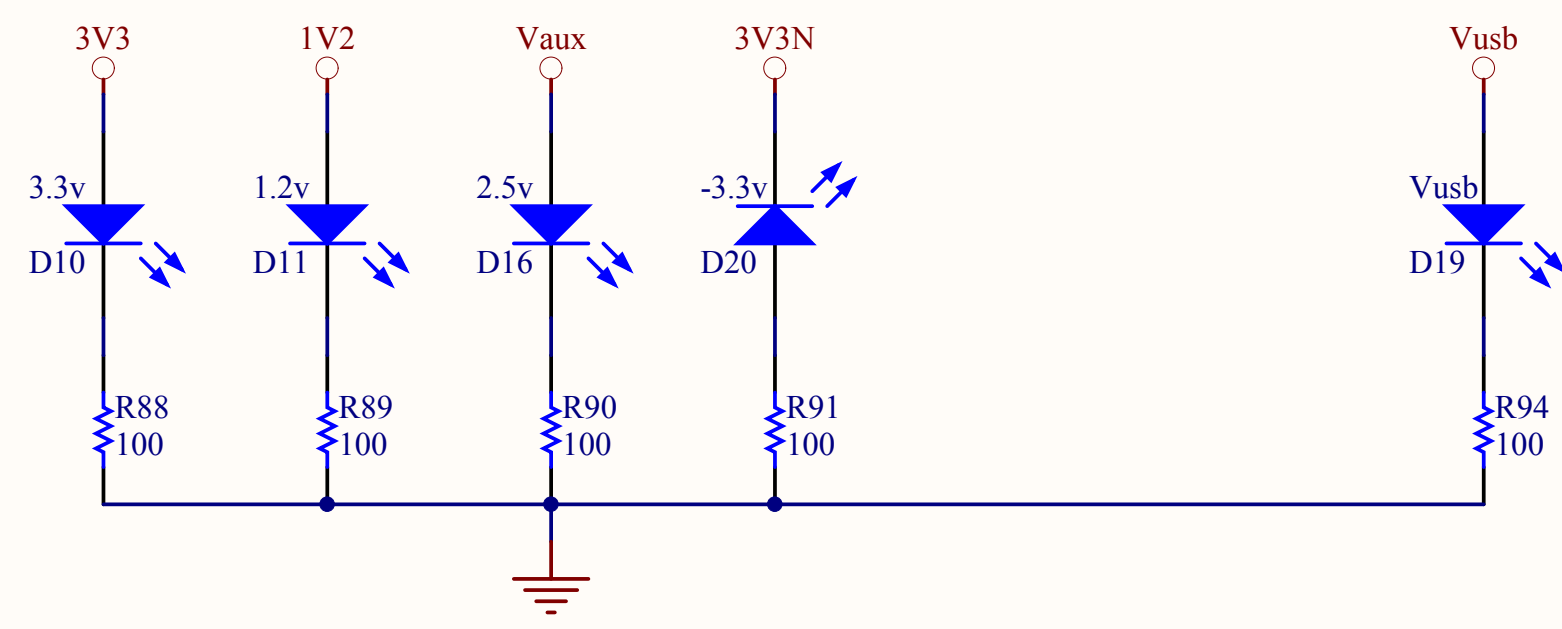
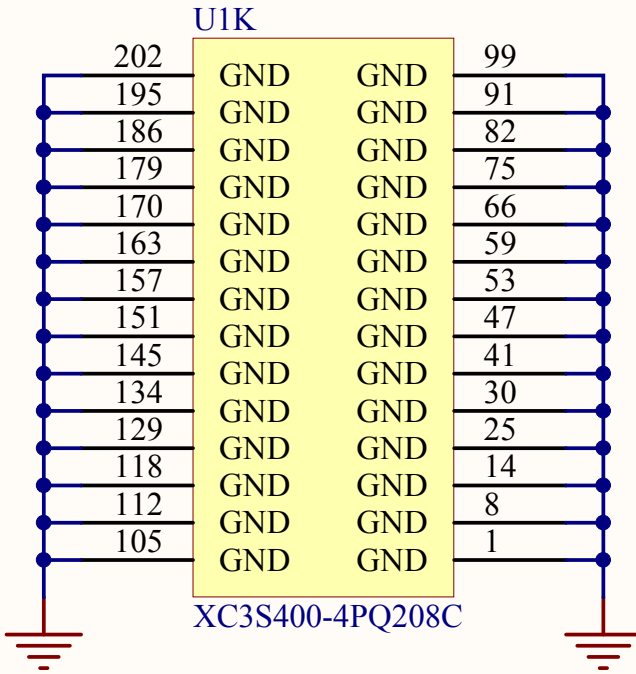
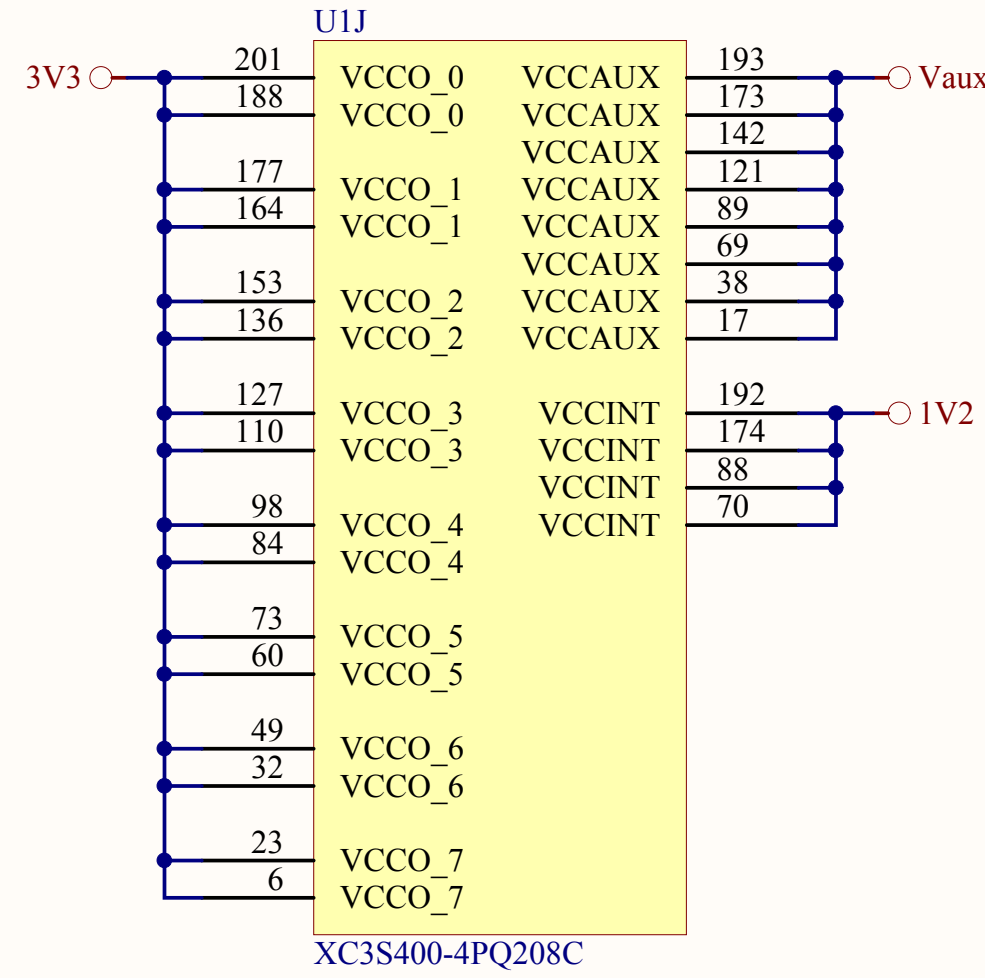
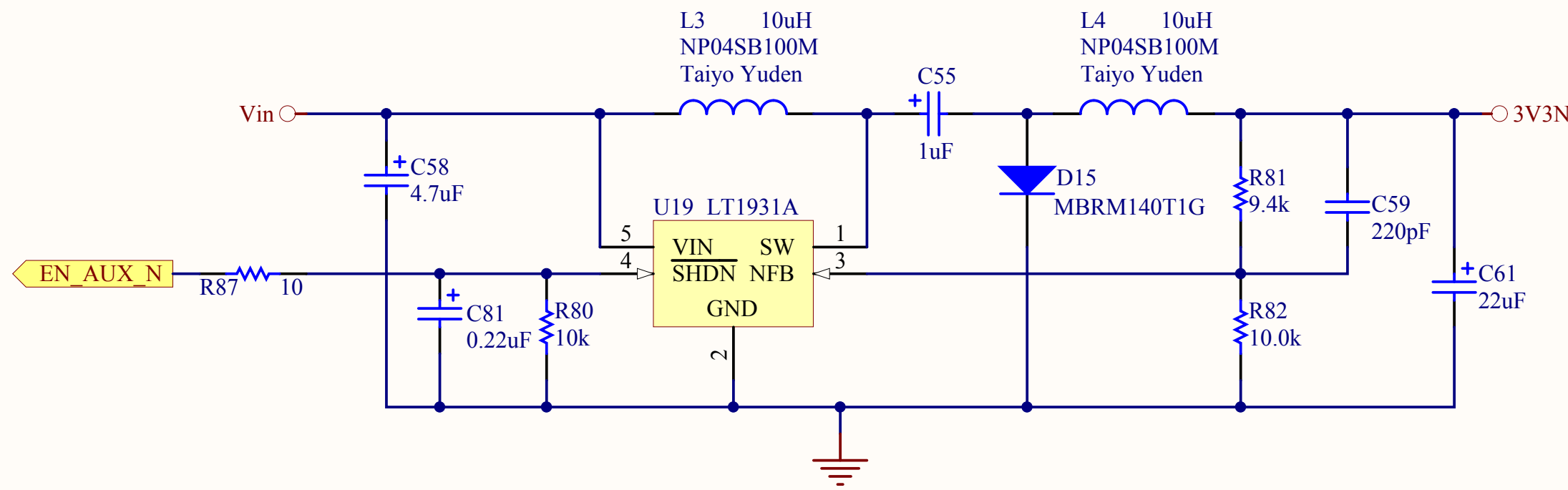
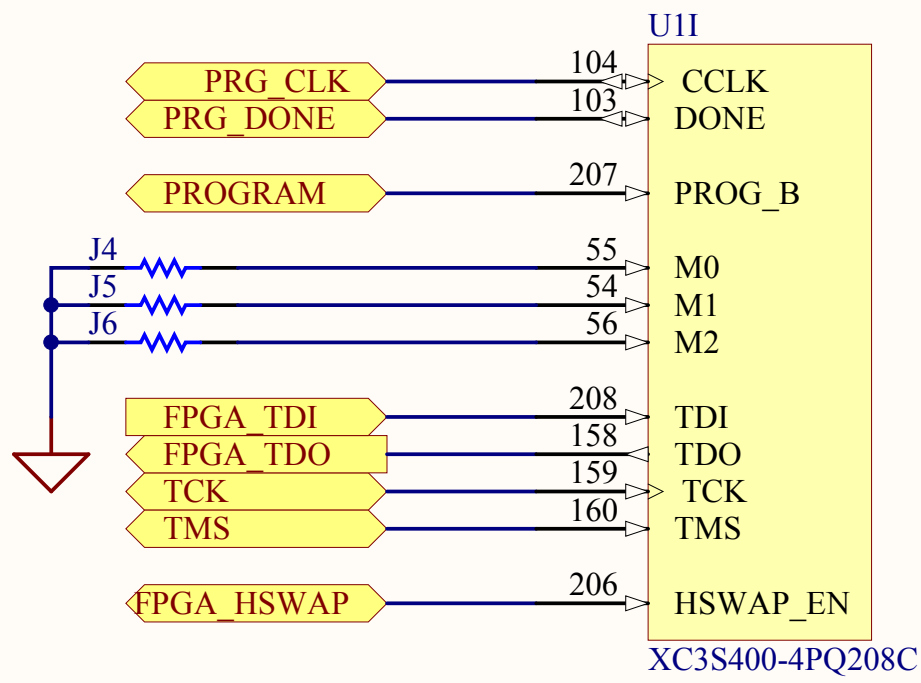
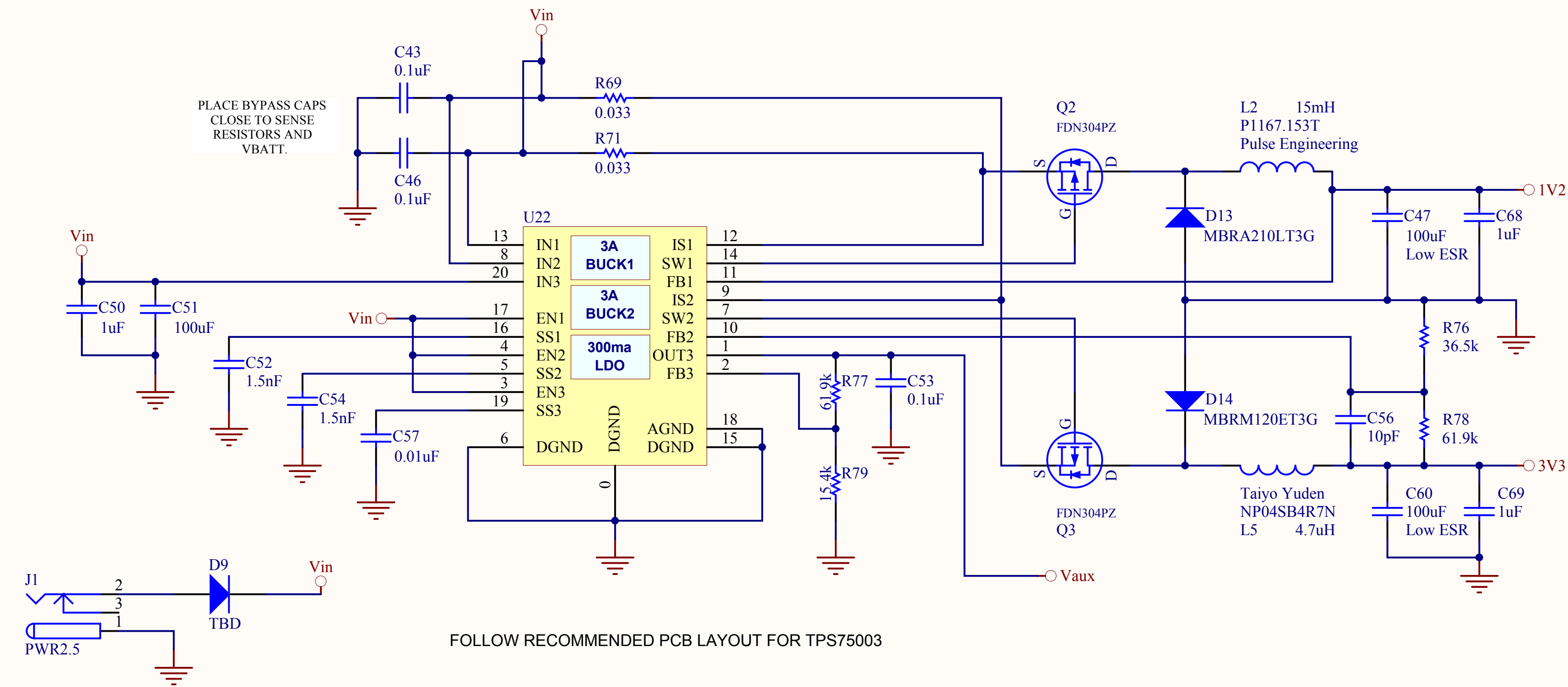
SD CARD



USER LEDs

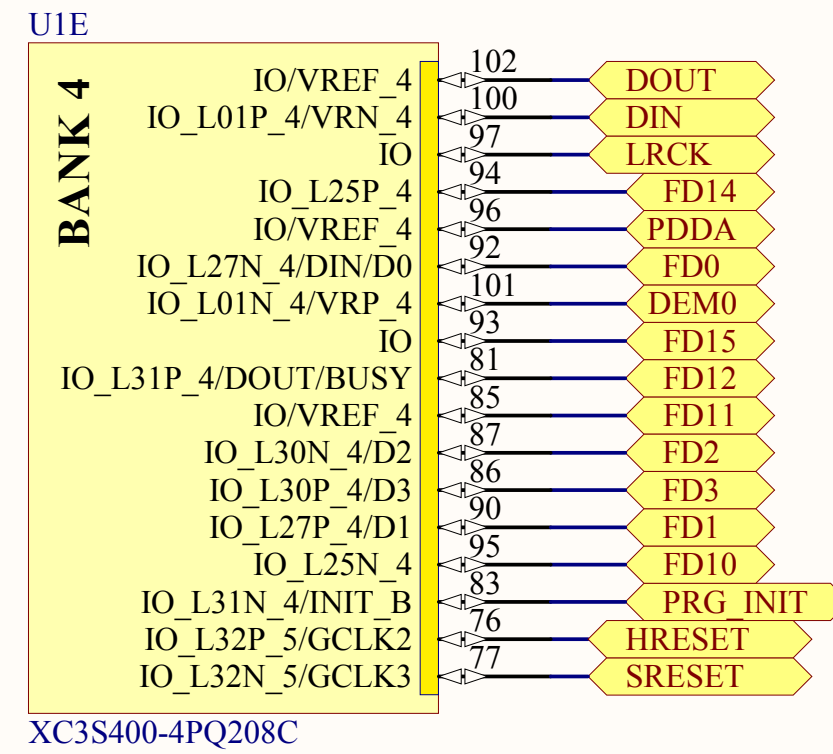
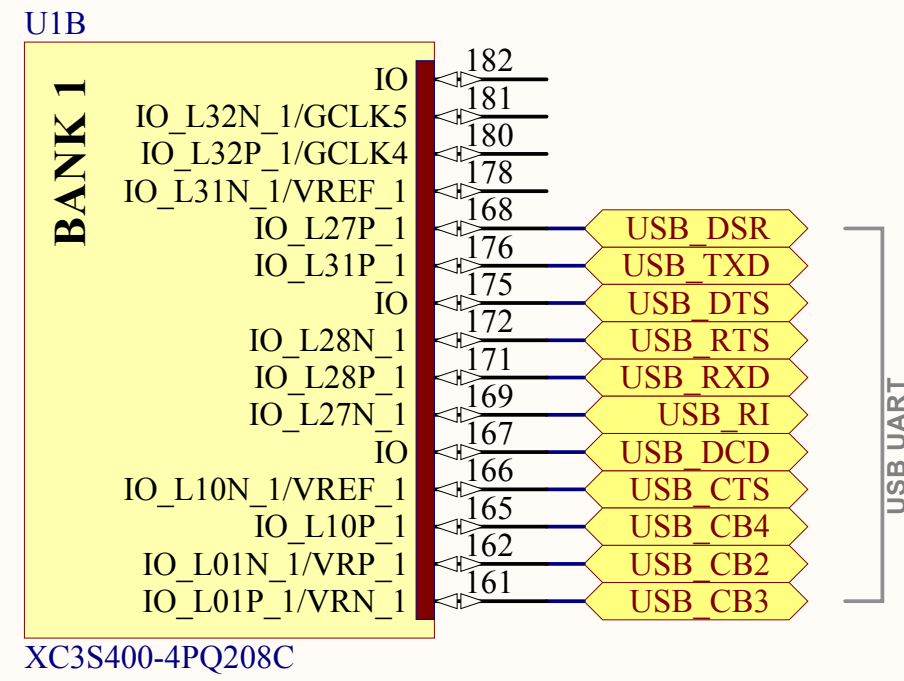
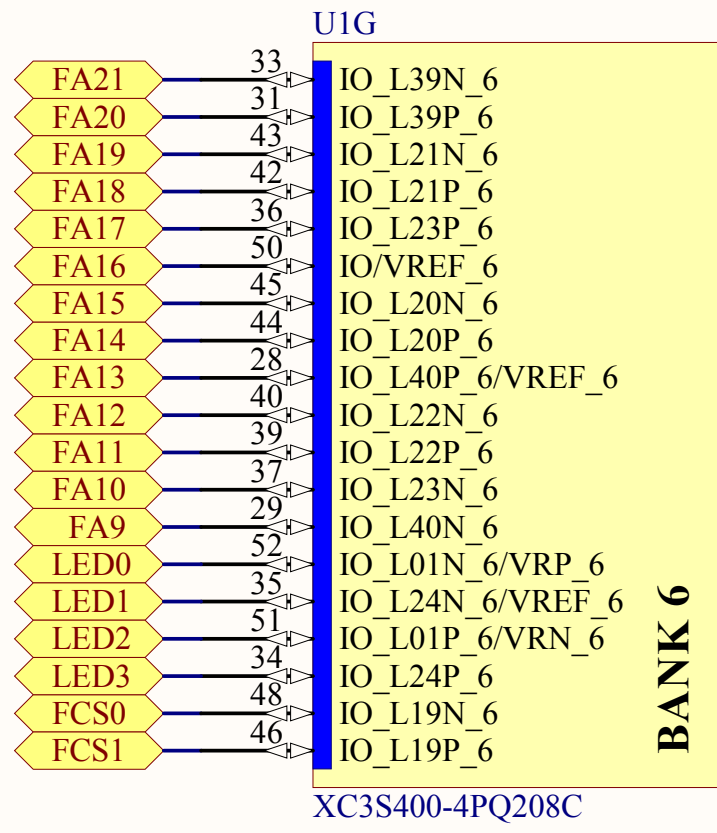
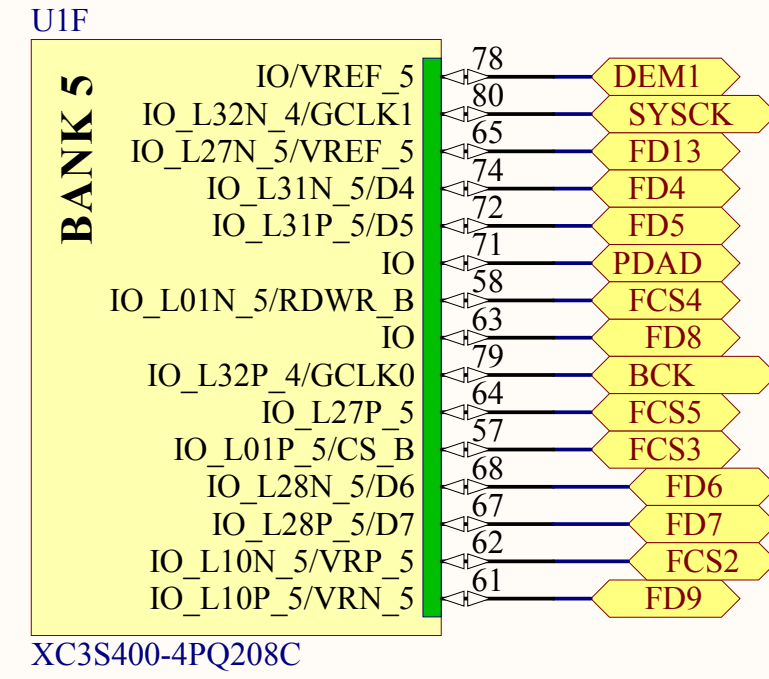
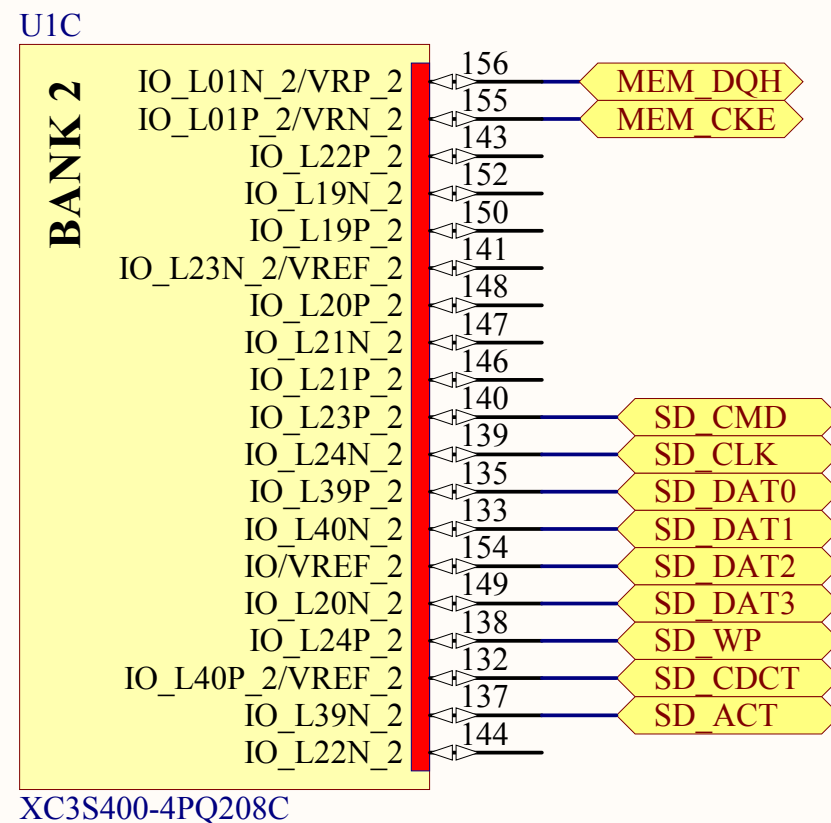
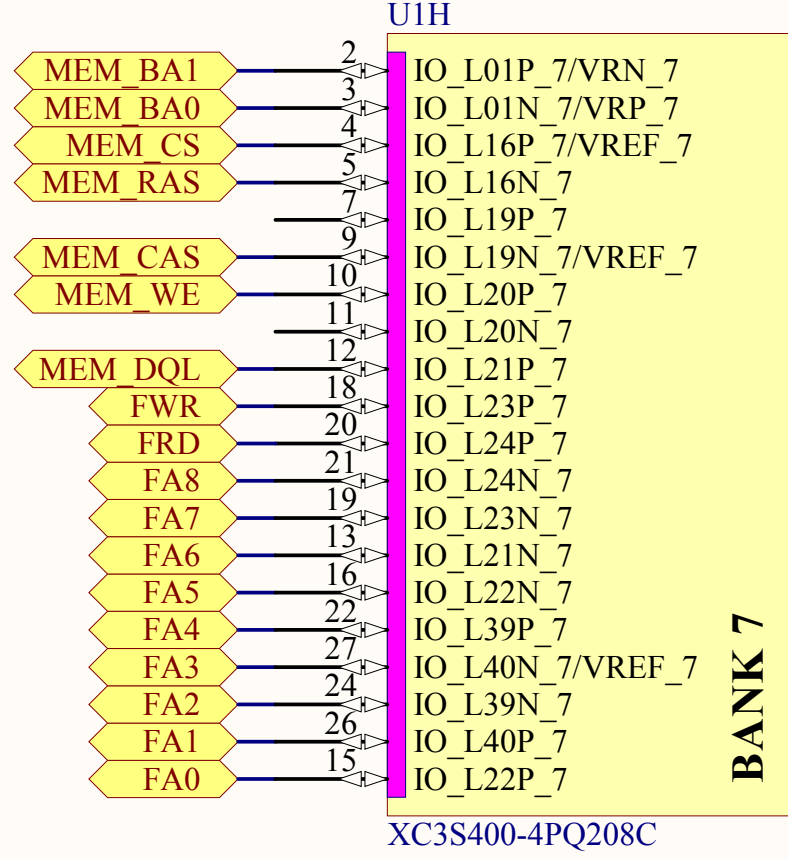
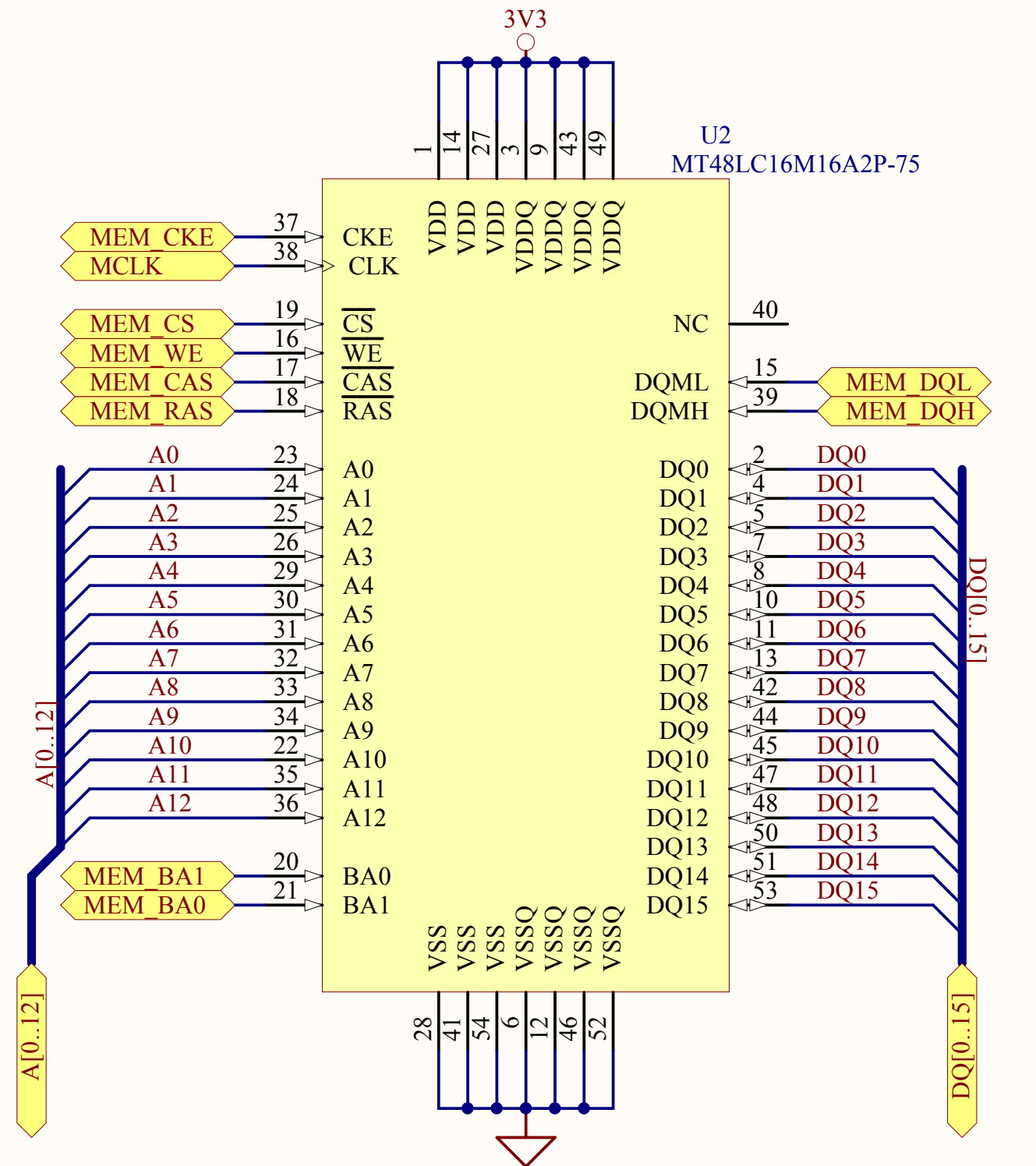
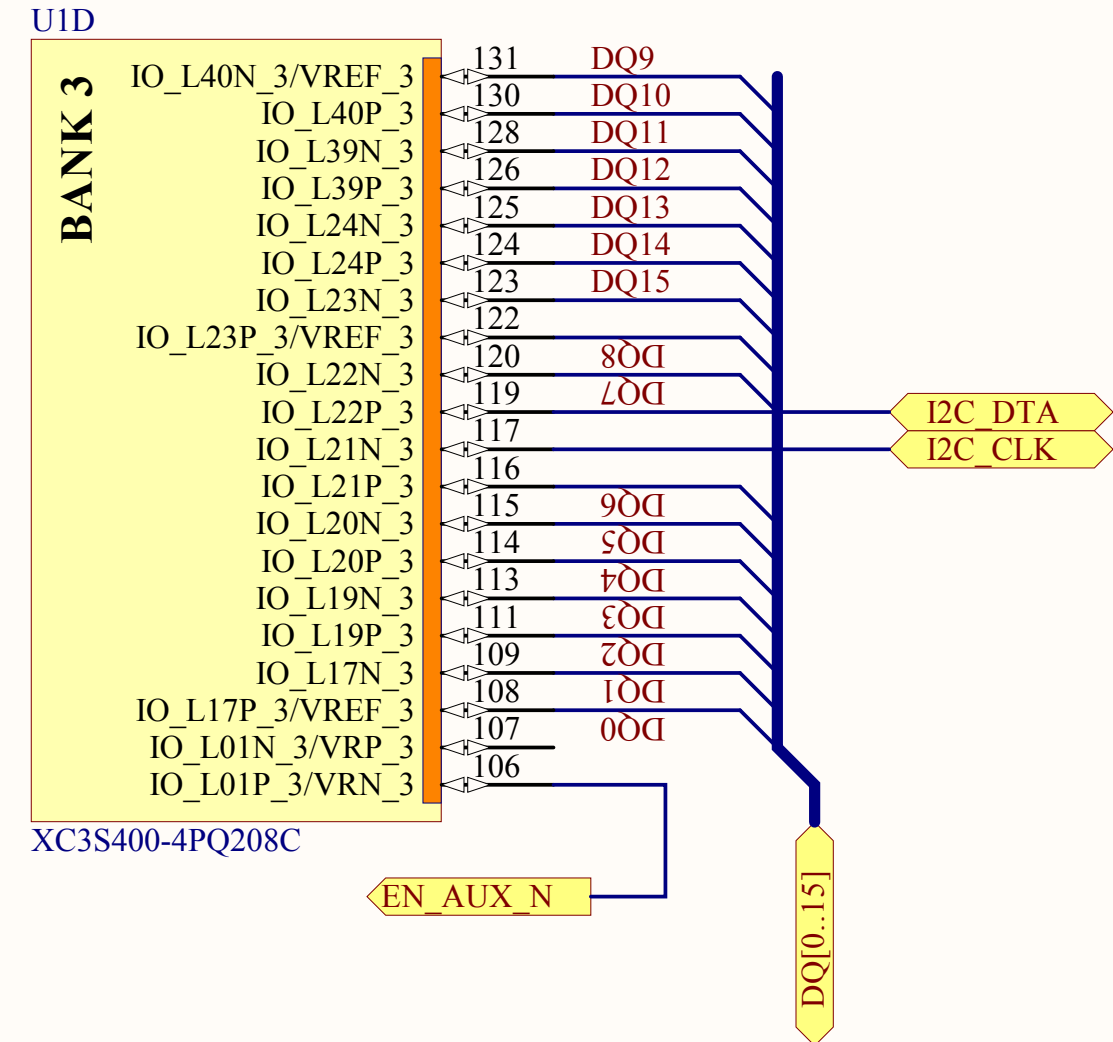
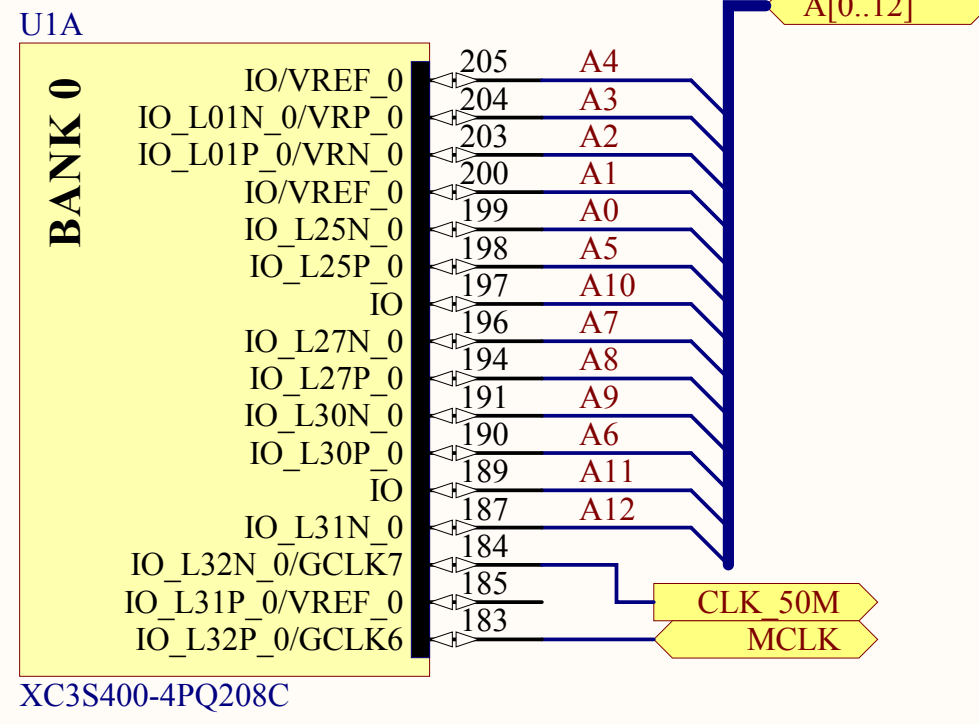


Title		COMMUNICATIONS	
Size	Number	Revision	
Tabloid			
Date:	4/7/2008	Sheet of	
File:	C:\projects\mimzy\altium\comm.SchDoc	Drawn By:	



Title		
FPGA TRIPLE POWER SUPPLY		
Size	Number	Revision
Tabloid		
Date:	4/7/2008	Sheet of
File:	C:\projects\...\fpga-power.SchDoc	Drawn By:

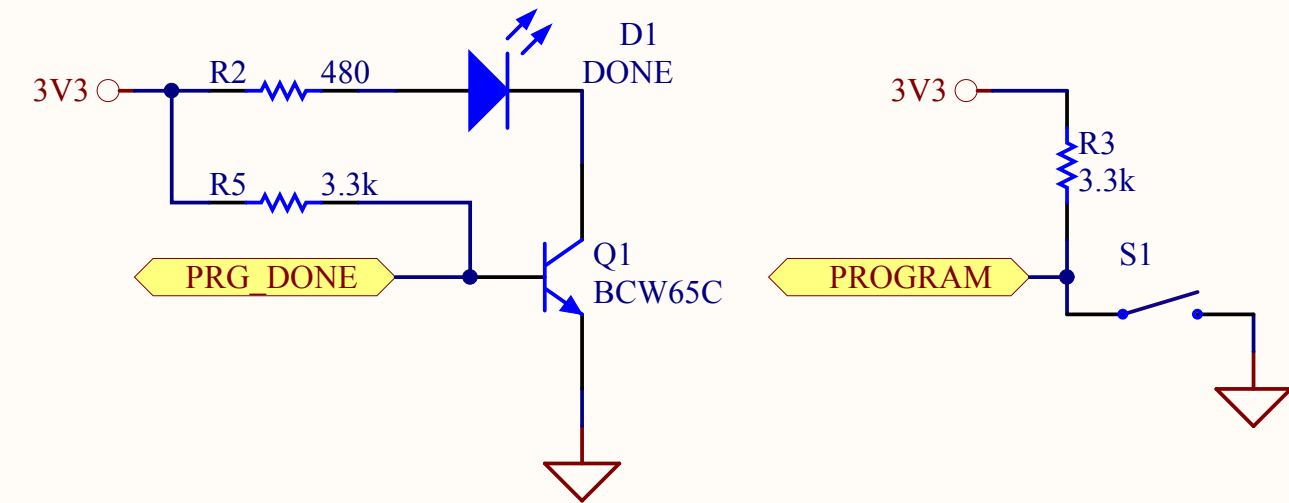
MUST HAVE MANY DECOUPLING CAPS YET
ADD A SIMPLE GPIO HEADER



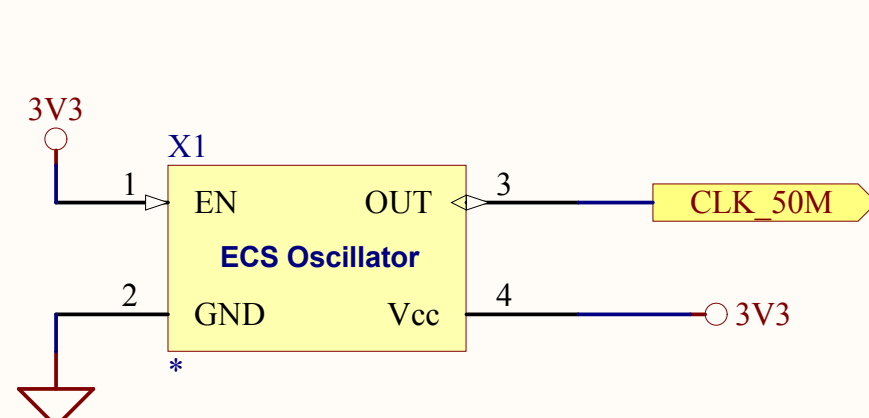
LPC PORT

TODO:
CONFIGURE HWSWAP PIN
REPLACE FLASH ROM WITH INTEL STRATAFLASH

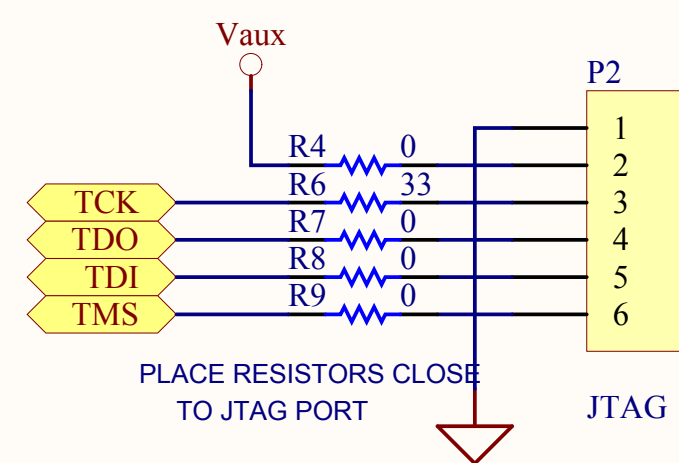
PROGRAM LED & BUTTON



CLOCK



JTAG HEADER



SOFTWARE JTAG

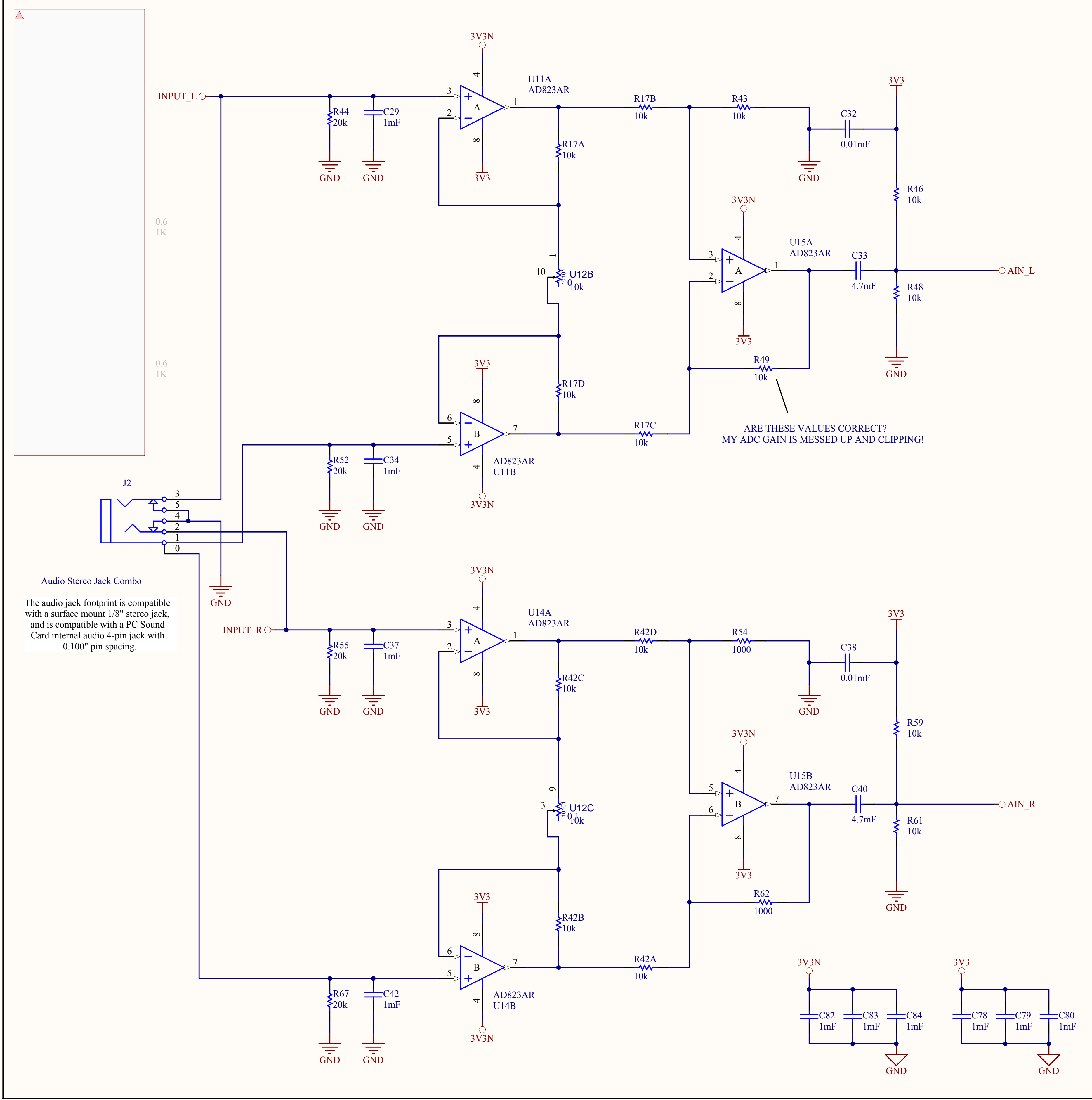
FPGA PINS ARE NOT
LOCKED YET!

CLOCK TERMINATION

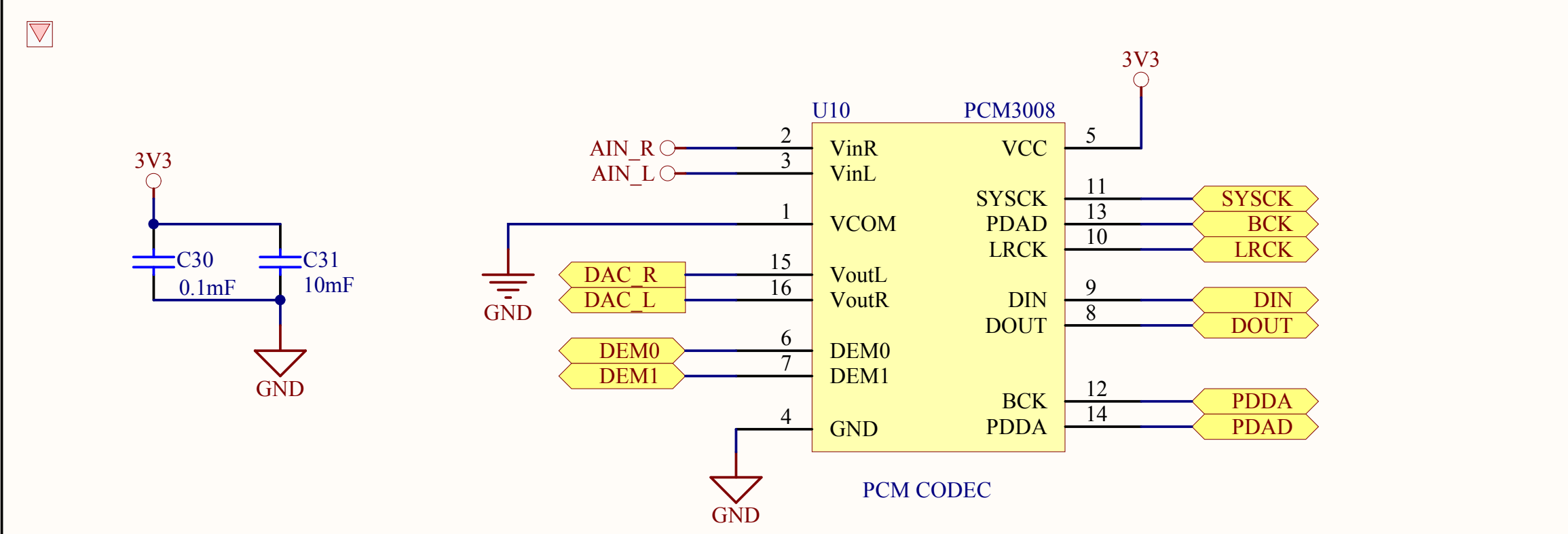


Title		
FPGA		
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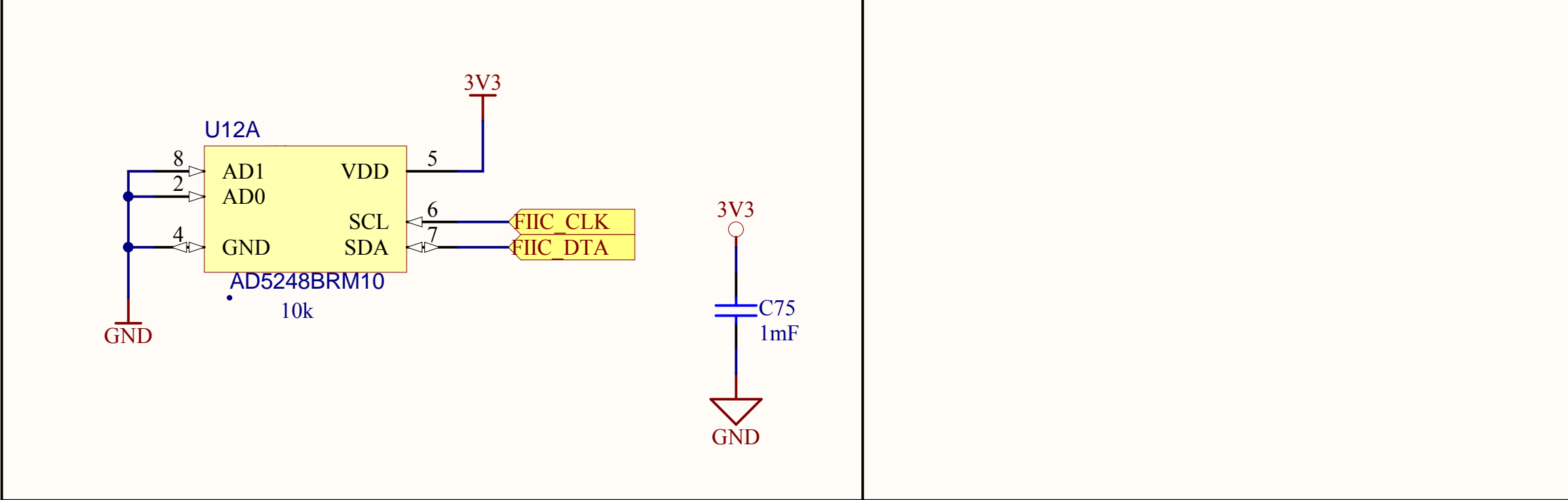
AUDIO DIFFERENTIAL INPUT AMPLIFIERS



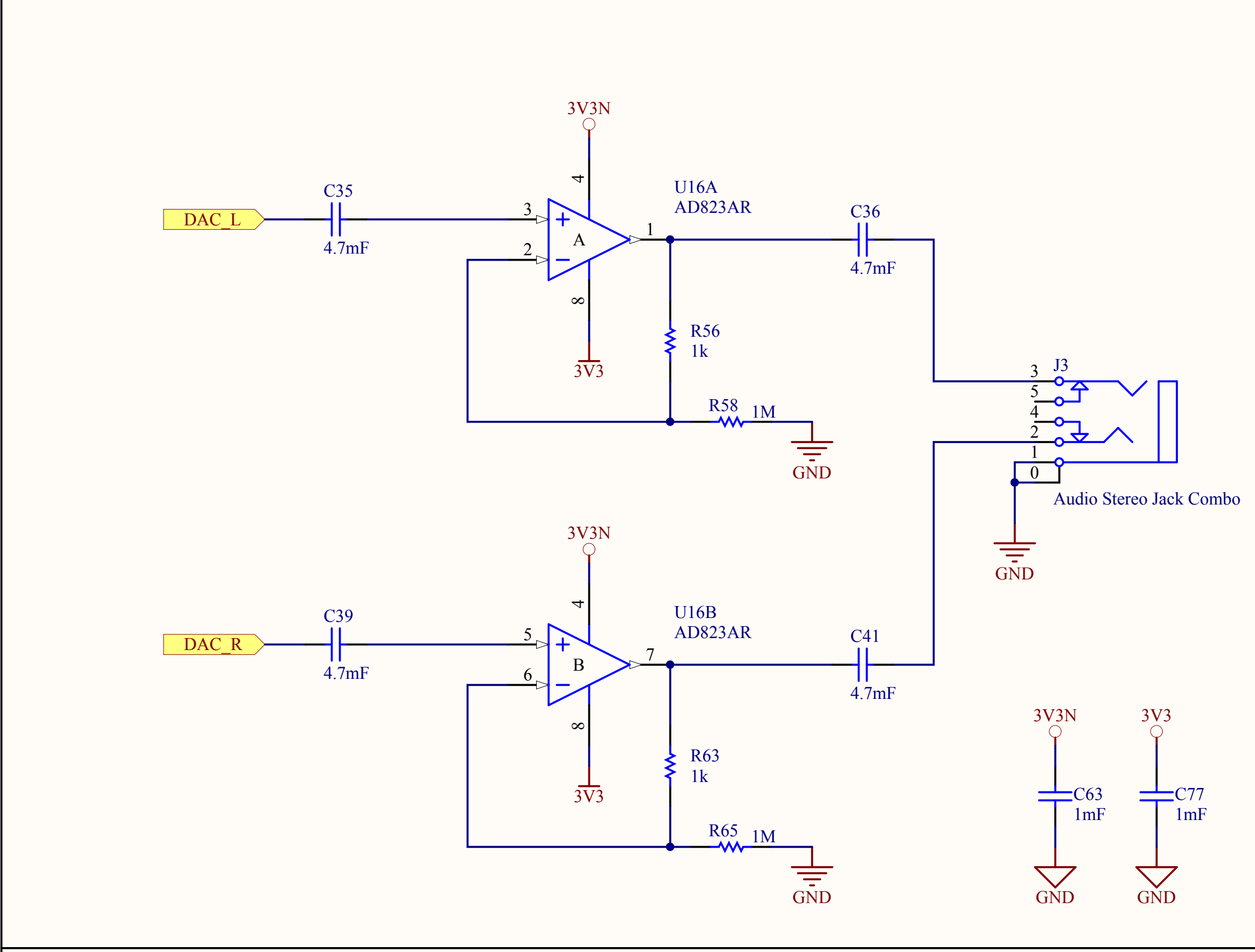
BURR BROWN PCM AUDIO CODEC



DIGITAL AUDIO INPUT GAIN CONTROL



AUDIO OUTPUT BUFFERS

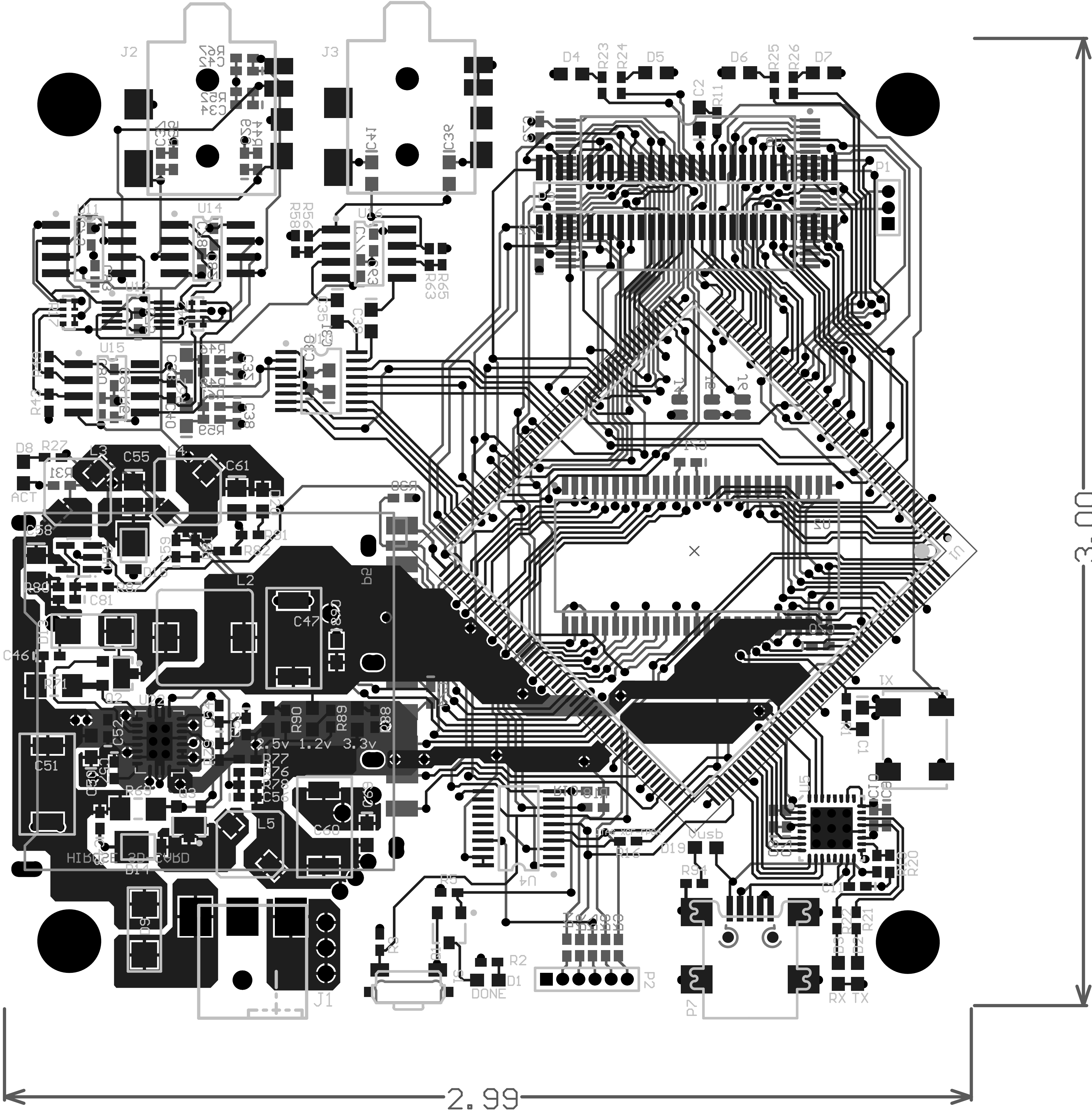


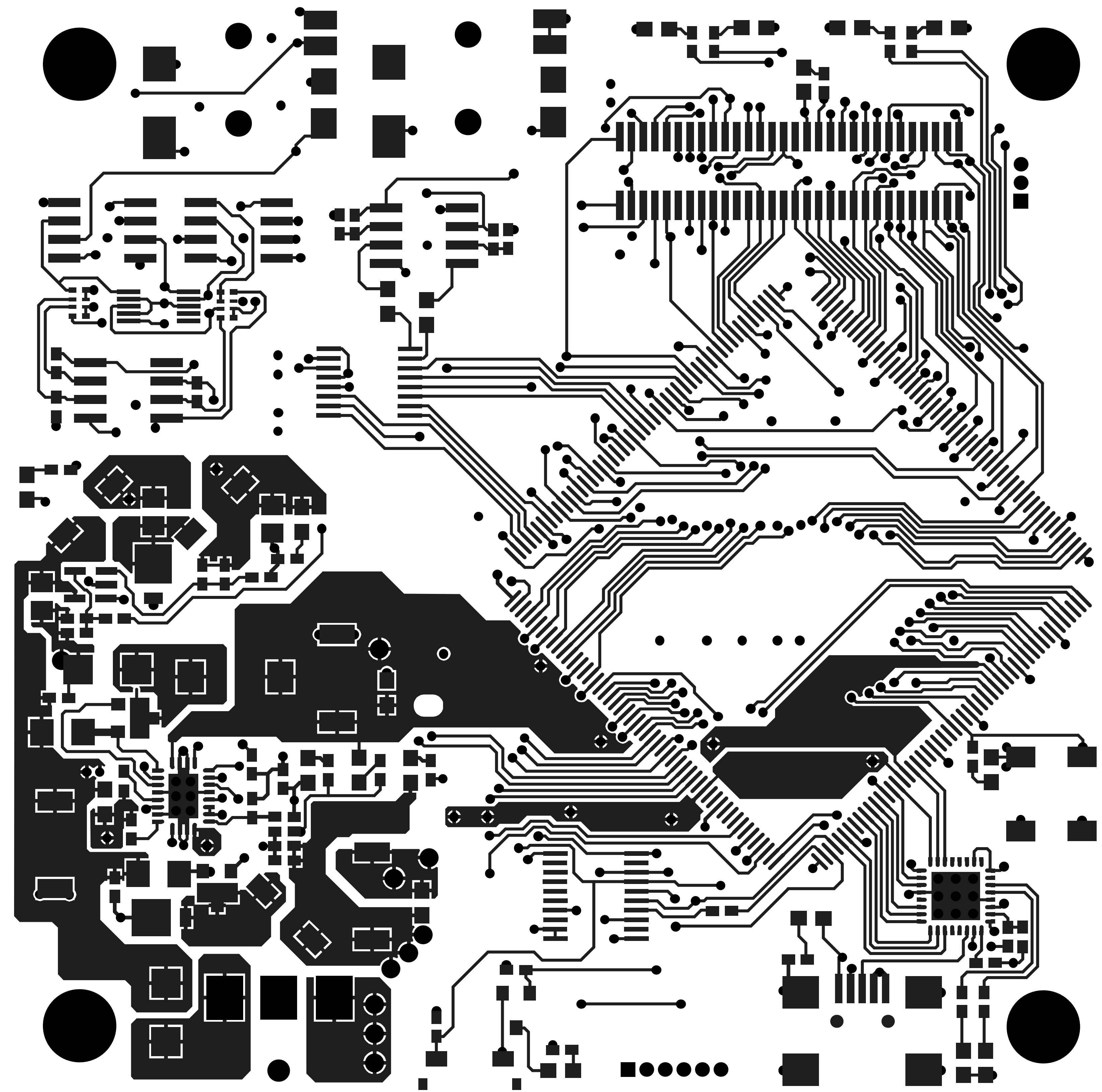
Title		
PCM AUDIO CODEC		
Size	Number	Revision
Tabloid		
Date:	4/7/2008	Sheet of
File:	C:\projects\pcm-codec.SchDoc	Drawn By:

NOTES

Spare Decoupling Caps

Caps/Res's under sd socket must be moved to top





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